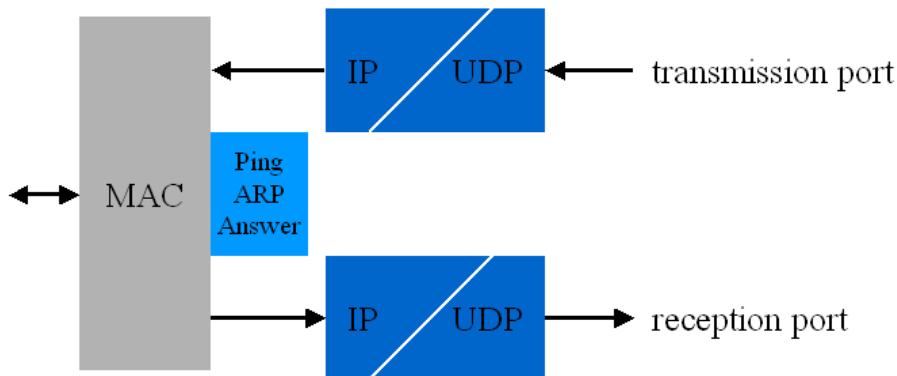


## Overview

UdpStack is a set of VHDL cores implementing UDP stack for FPGA applications, UdpStack is the ultimate Offload Engine.

The UdpStack\_Lite is a small footprint version but still handle all the necessary functionalities of UDP and IP protocol. In addition ARP answer and PING answer, allowing an easy and fast integration communication with other systems.

UdpStack is directly connectable to Xilinx TEMAC or we can add our own MAC if your FPGA does not include one. We always provide one or more application examples, some demo boards are directly supported by our application examples.



## Features

High performances but low resources :

- “Easy To Use”, a real plug and play module
- Up to 40% of the link bandwidth
- Protocols : UDP, IP, ARP and PING answer
- No Jumbo frames
- Handle clock domain crossing, for direct connection to user clock domain
- Provided with 16 dedicated MAC addresses

## Applications

- Low cost systems
- High density design
- Debug Interface
- Video Over IP, MPEG-TS over UDP/IP
- Voice Over IP, SIP/RTP over UDP/IP