UdpStack Full
Product Brief

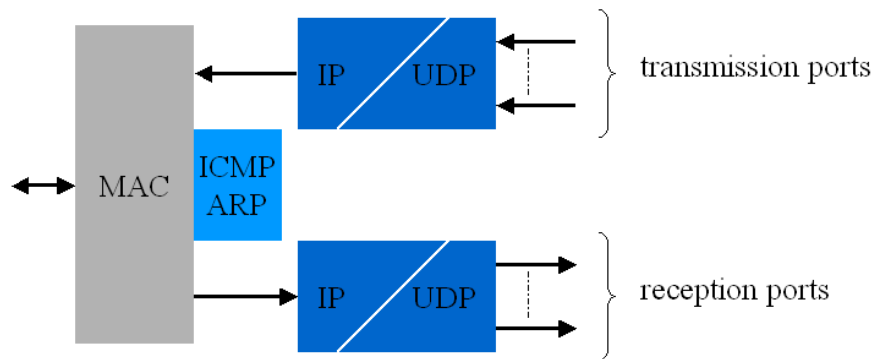## Overview

UdpStack is a set of VHDL cores implementing UDP stack for FPGA applications, UdpStack is the ultimate Offload Engine.

The UdpStack_Full version allows user to communicate trough many "sockets" (multi Rx/Tx ports) without requiring a processor.
UdpStack_Full handle UDP protocol, IP protocol and a part of the ARP and ICMP, allowing easier final user system integration.
UdpStack is directly connectable to Xilinx TEMAC or we can add our own MAC if your FPGA does not include one. We always provide one or more application examples, some demo boards are directly supported by our application examples.

Features

High performances but low resources :
- "Easy To Use", a real plug and play module
- High speed, user can reach the full link bandwidth
- Protocols : UDP, IP, ARP, ICMP
- Support Jumbo frames
- Handle clock domain crossing, for direct connection to user clock domain
- Provided with 256 dedicated MAC addresses

Applications

- Video Over IP, MPEG-TS over UDP/IP
- Voice Over IP, SIP/RTP over UDP/IP
- High throughput data transfer, between FPGA and/or PC
- Systems with or without processor
- Debug Interface